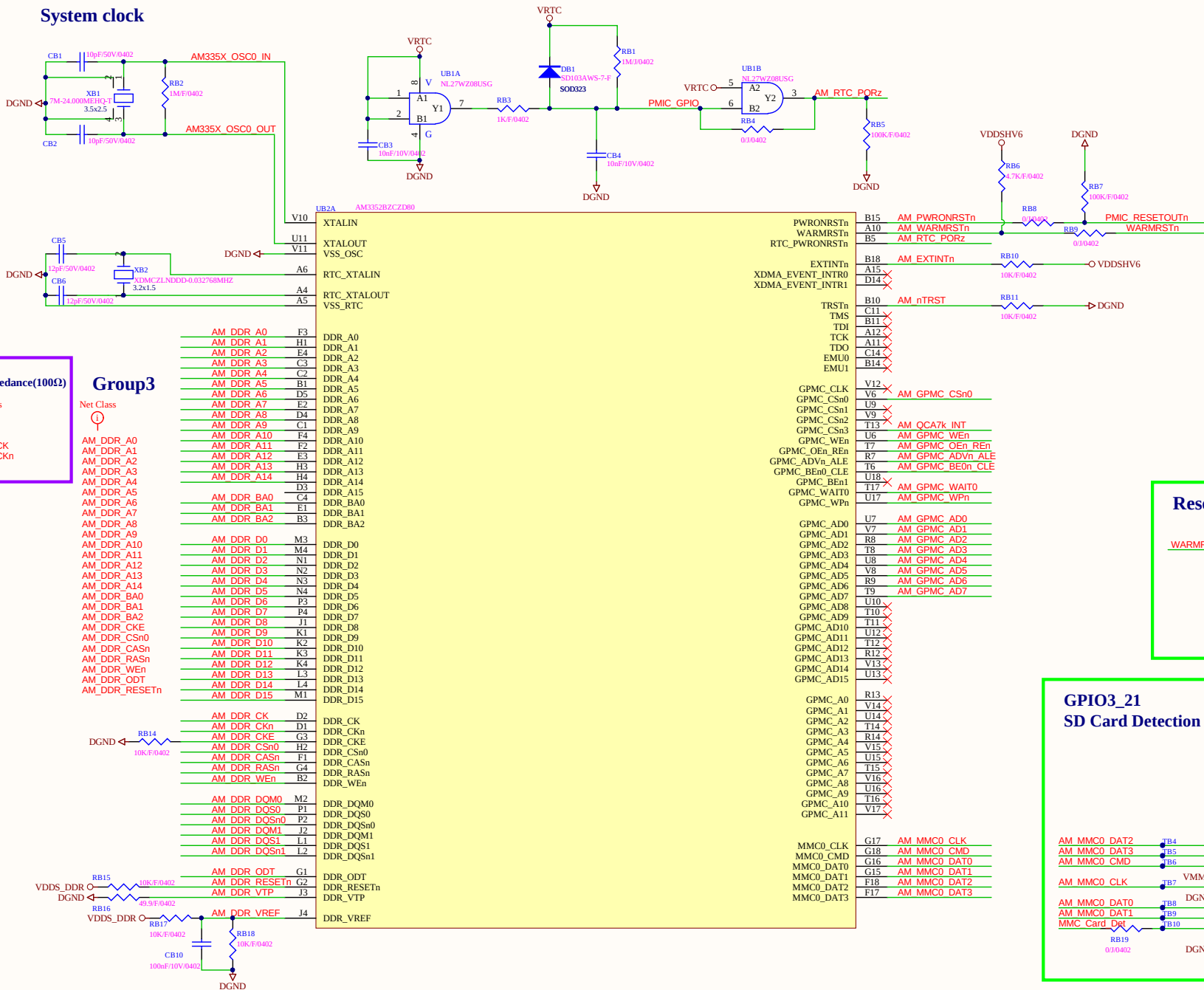


System clock



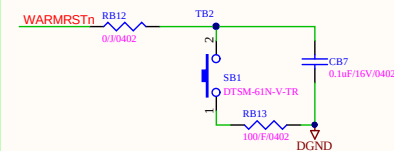
Differential impedance(100Ω)

Net Class
①
AM_DDR_CK
AM_DDR_CKn

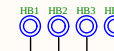
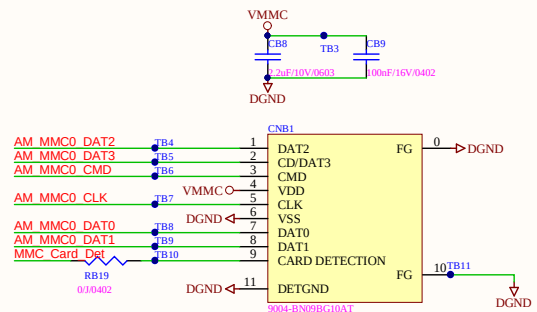
Group3

Net Class
①
AM_DDR_A0
AM_DDR_A1
AM_DDR_A2
AM_DDR_A3
AM_DDR_A4
AM_DDR_A5
AM_DDR_A6
AM_DDR_A7
AM_DDR_A8
AM_DDR_A9
AM_DDR_A10
AM_DDR_A11
AM_DDR_A12
AM_DDR_A13
AM_DDR_A14
AM_DDR_BA0
AM_DDR_BA1
AM_DDR_BA2
AM_DDR_CKE
AM_DDR_CKEn
AM_DDR_CSn0
AM_DDR_CASn
AM_DDR_RASn
AM_DDR_ODT
AM_DDR_RESETn

Reset Button Circuit

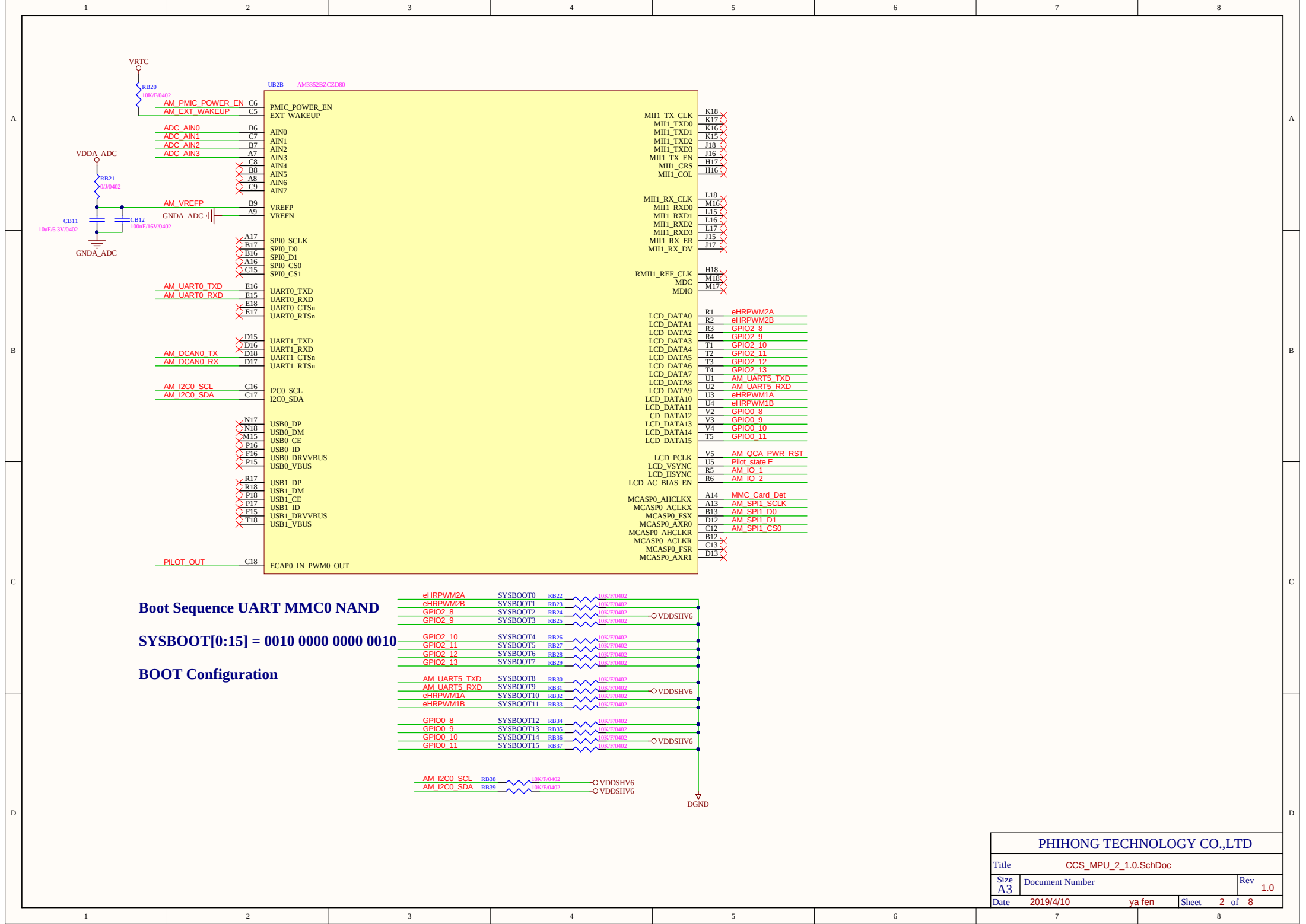


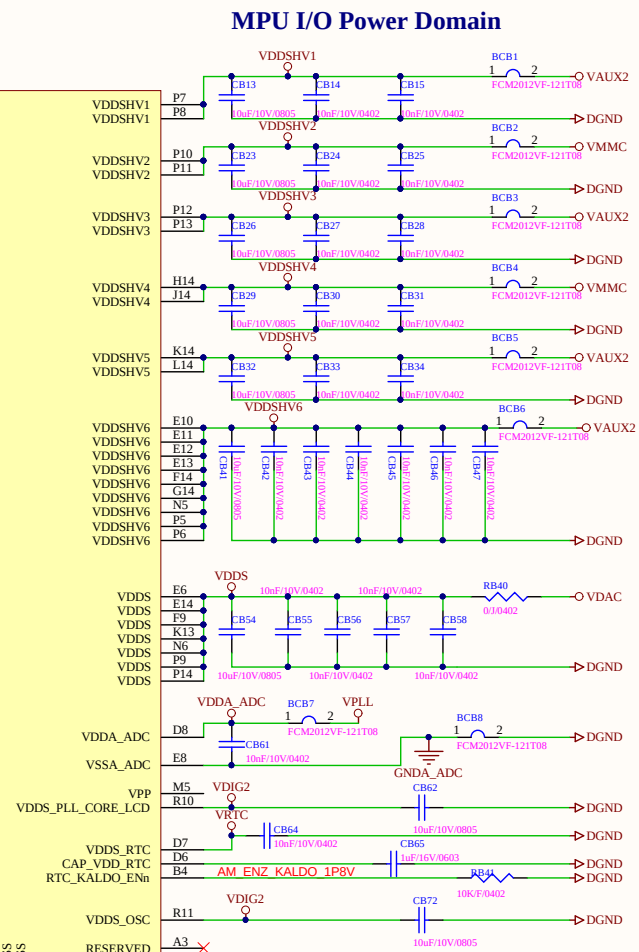
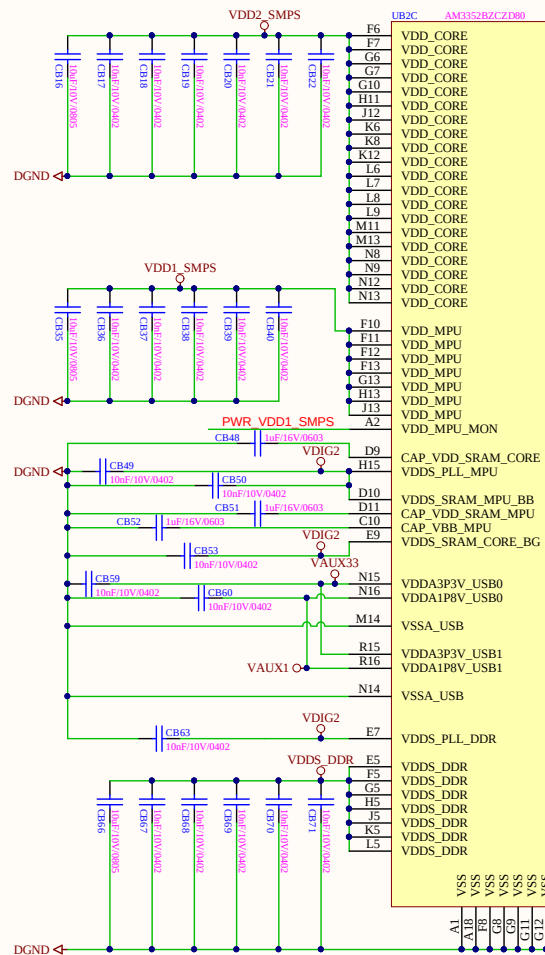
GPIO3_21 SD Card Detection



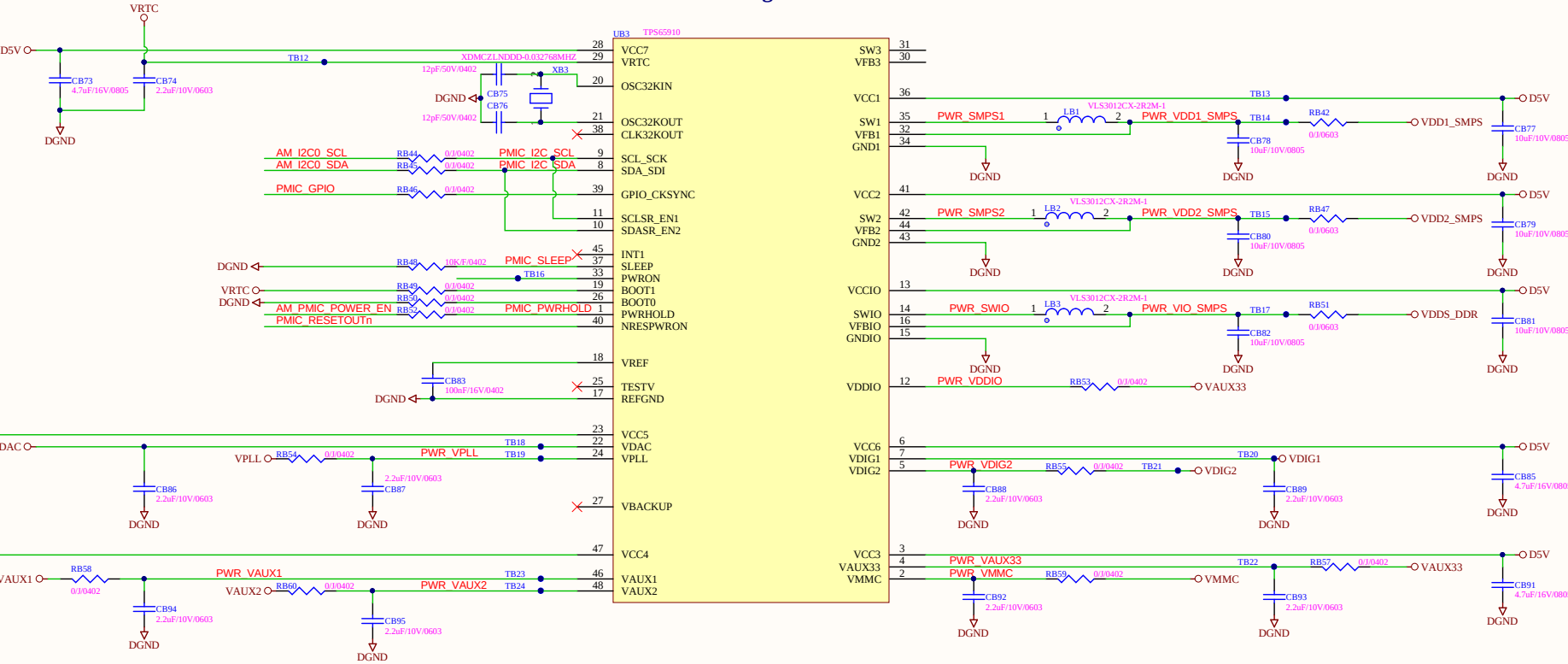
PHIHONG TECHNOLOGY CO.,LTD

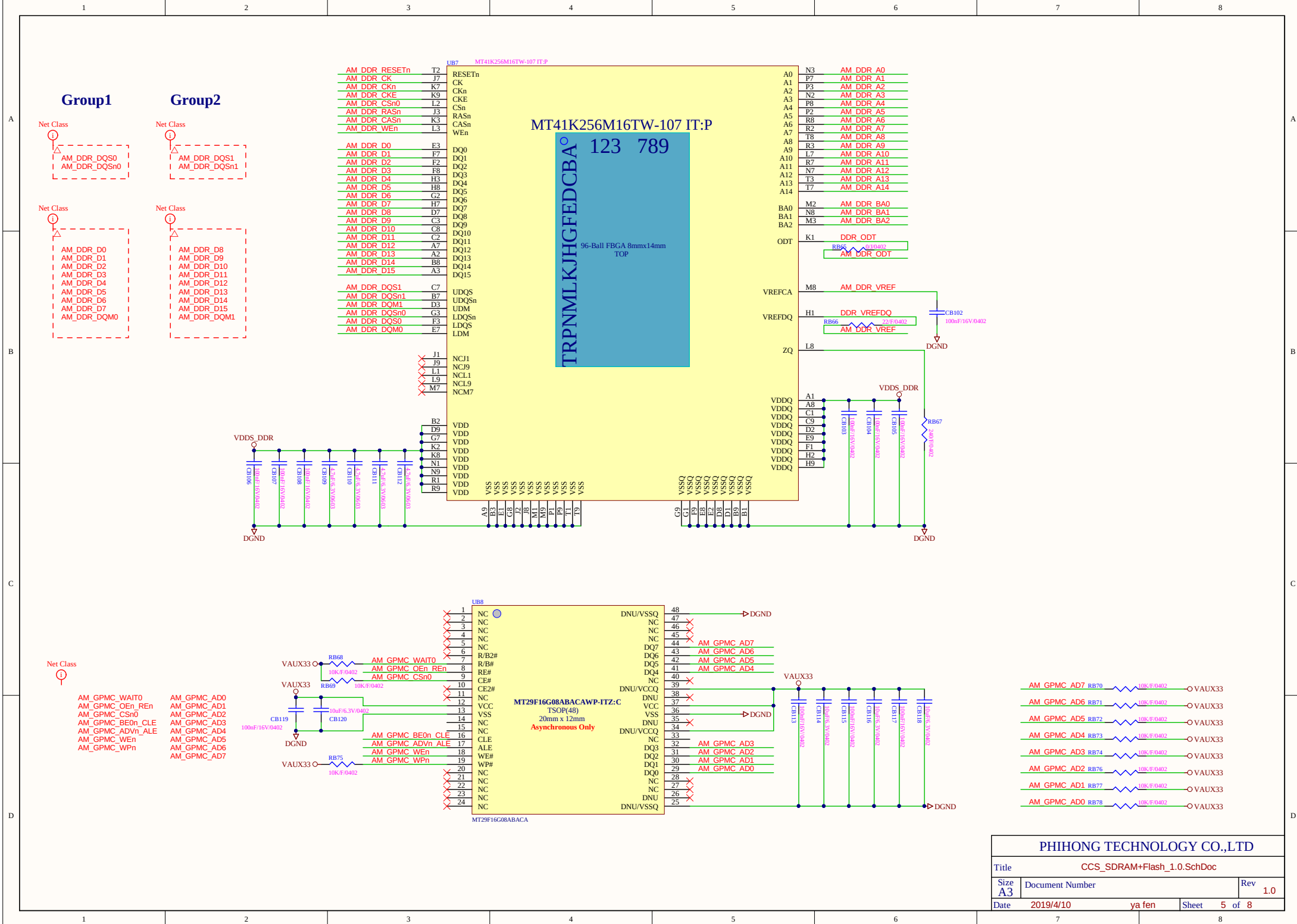
Title	CCS_MPU_1_1.0.SchDoc		
Size	Document Number	Rev	
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Date	2019/4/10	ya fen	Sheet 1 of 8

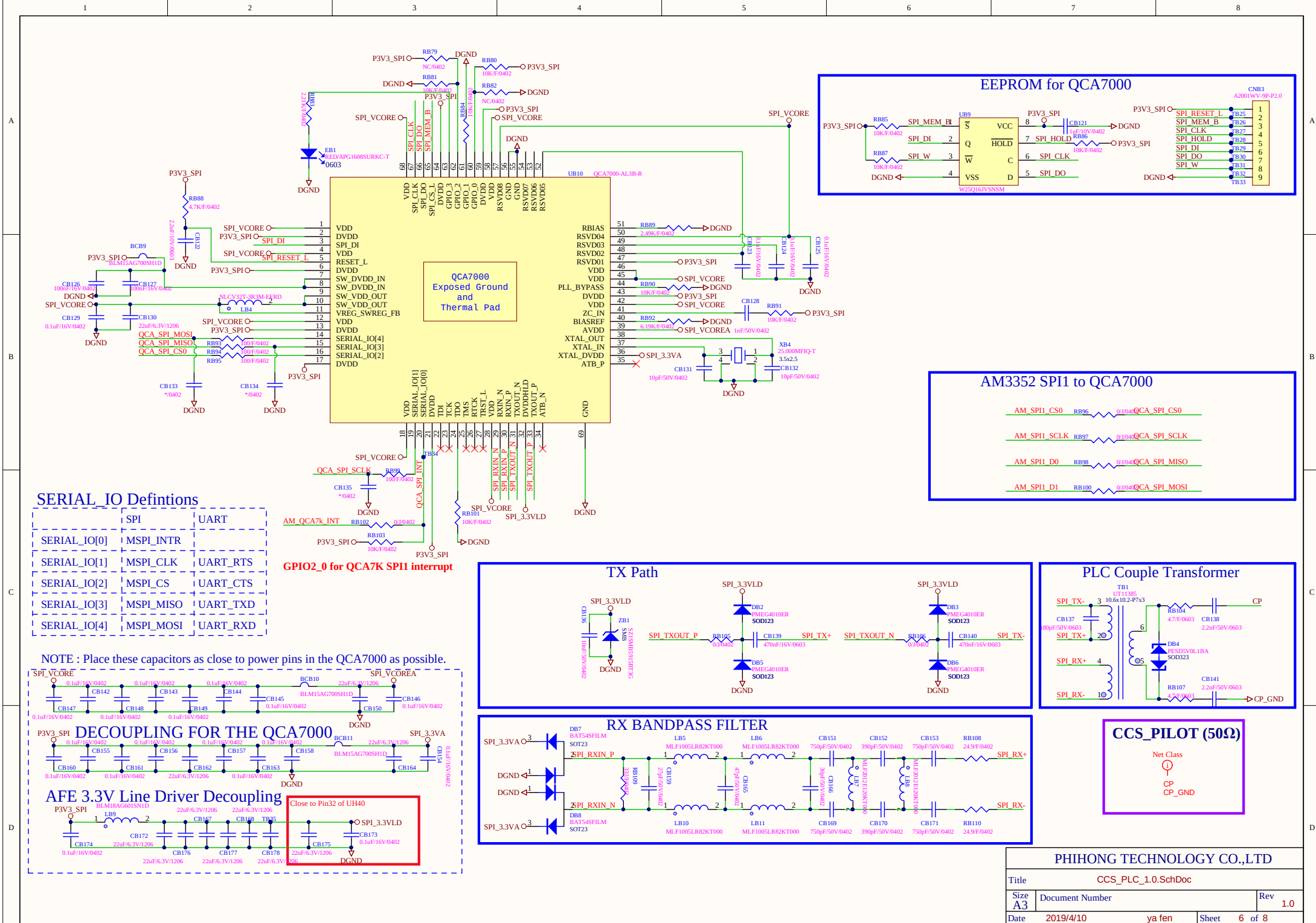




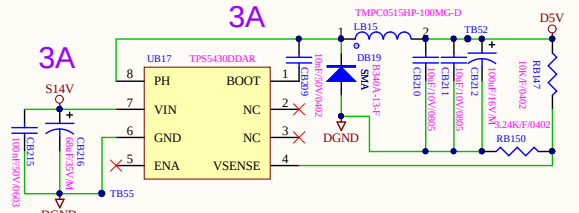
Power Management IC





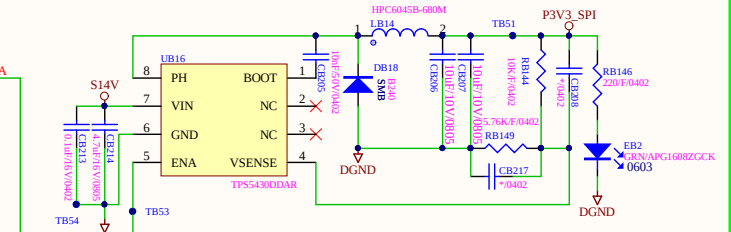
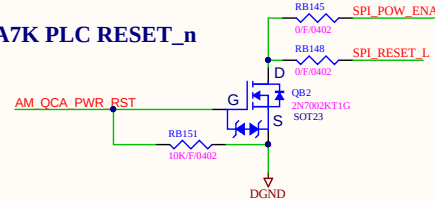


S14V to D5V Buck for PMIC



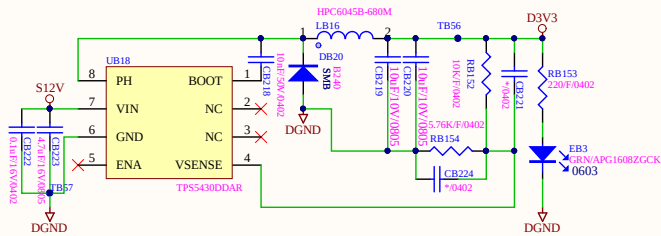
$$D5V = V_{ref} \times (1 + RH30/RH32) = 1.221 \times (1 + 10/3.24) = 4.99$$

QCA7K PLC RESET_n



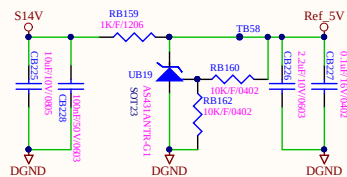
$$P3V3 = V_{ref} \times (1 + RH46/RH49) = 1.221 \times (1 + 10/5.76) = 3.34$$

S12V to D3V3

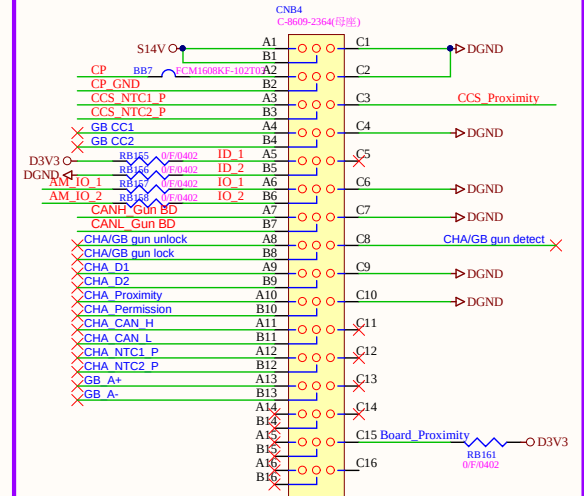


$$P3V3 = V_{ref} \times (1 + RH46/RH49) = 1.221 \times (1 + 10/5.76) = 3.34$$

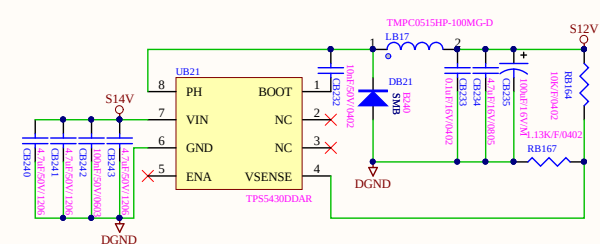
S14V to Ref_5V LDO for Control Pilot Offset circuit



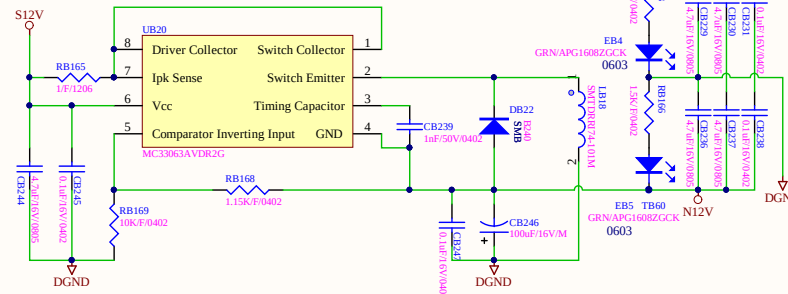
Interface for Communication Board



PE_P12V /PE_N12V Power for control pilot circuit



$$PE_P12V = V_{ref} \times (1 + RH353/RH354) = 1.221 \times (1 + 10/1.13) = 12.03$$

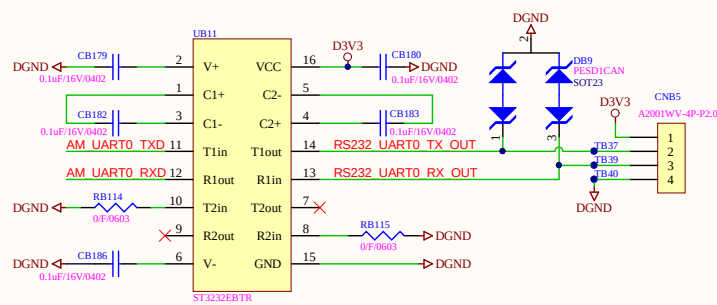


$$PE_N12V = V_{ref} \times (1 + RH48/RH45) = 1.25 \times (1 + 10/1.15) = 12.12$$

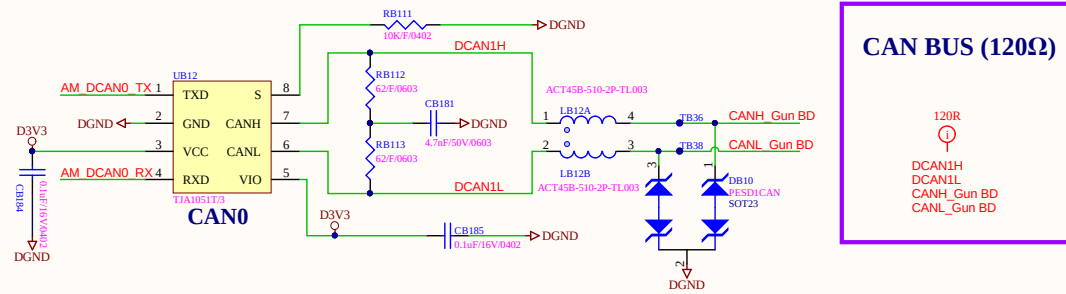
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Title	CCS_Power Back+Connector_1.0.SchDoc		
Size	Document Number	Rev	
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Date	2019/4/10	ya fen	Sheet 8 of 8

RS232(UART0-Debug/UART2-RFID)



CAN0 for Communication Board



CAN BUS (120Ω)

120R
DCAN1H
DCAN1L
CANH_Gun BD
CANL_Gun BD

GUN

CCS GUN Temp 1

